

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q88659

Thomas Meredith BROWN, et al.

Appln. No.: Based on PCT/GB2003/005435

Confirmation No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filed: June 14, 2005

Examiner: Not Yet Assigned

For: ELECTRONIC DEVICES

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith, except for the following: U.S. patents and/or U.S. patent publications; and co-pending non-provisional U.S. applications filed after June 30, 2003.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after

INFORMATION DISCLOSURE STATEMENT
U.S. Appln. No.:

10/538870
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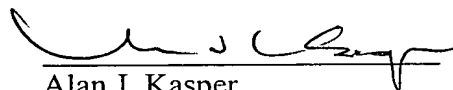
filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith a copy of a Communication from a foreign patent office in a counterpart application citing such documents, together with an English-language version (if not already included) of at least that portion of the Communication indicating the degree of relevance found by the foreign patent office.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

Respectfully submitted,



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WASHINGTON OFFICE

23373

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Date: June 14, 2005

Substitute for Form 1449 A & B/PTO <u>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</u> <i>(use as many sheets as necessary)</i>			Complete if Known Application Number [case serial number] 10/4538870 Confirmation Number Not Yet Assigned Filing Date June 14, 2005 First Named Inventor Thomas Meredith BROWN Art Unit Not Yet Assigned Examiner Name Not Yet Assigned Attorney Docket Number Q88659		
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US			
		US			
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ <i>(if known)</i>			
		WO	02/29912	A	04/11/2002	Smith Paul	
		WO	02/47183	A	06/13/2002	Kurz Heinrich	
		WO	01/60589	A	08/23/2001	Stutzmann Natalie	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		STRINGHAUS H ET AL: "HIGH-RESOLUTION INK-JET PRINTING OF ALL-POLYMER TRANSISTOR CIRCUITS" MRS. BULLETIN, PITTSBURGH, US, VOL. 26, NO. 7, JULY 2001, PAGES 539-543, XP001032496	
		GARNIER FRANCIS ETAL: "VERTICAL DEVICE ARCHITECTURE BY MOLDING OF ORGANIC-BASED THIN FILM TRANSISTOR" APPLIED PHYSICS LETTER, AMERICAN INSTITUTE PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, VOL. 73, NO. 12, September 21, 1998, PAGES 1721-1723, XP01202100	
		KAGAN C R ET AL: "PATTERNING ORGANIC-INORGANIC THIN-FILM TRANSISTORS USING MICROCONTRACT PRINTED TEMPLATES" APPLIED PHYSICS. NEW YORK, US, VOL. 79, NO. 21, November 19, 2001, PAGES 3536-3538, XP001061204	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to indicate here if English language Translation is attached.